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CONFIRMATION NO. 9075

SERIAL NUMBER 10/528,961	FILING OR 371(c) DATE 03/22/2005 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 05170/HG	
APPLICANTS Katsura Hirai, Tokyo, JAPAN;					
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/12140 09/24/2003					
** FOREIGN APPLICATIONS ***** JAPAN 2002-278599 09/25/2002					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged _____ Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 74	INDEPENDENT CLAIMS 4
ADDRESS 01933					
TITLE Electrical circuit, thin film transistor, method for manufacturing electric circuit and method for manufacturing thin film transistor					
FILING FEE RECEIVED 3800	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		